TE MECA SZM-V (CBSGS) Sub! - E-S. Dt-19-12-16.

QP CODE: 839002

(3	Hou	rs) (Total Marks	(Total Marks: 80	
(2)		 (1) Question No. 1 is compulsory. (2) Solve any three questions from remaining five questions. (3) Draw neat diagrams and assume suitable data wherever necessary. Justify your assum 	your assumption	
1.	(a) (b) (c) (d) (e)	Attempt any four: Draw the block diagram of microcontroller and microprocessor. Draw the format for interrupt enable register with function of each bit. List the features of ARM7 microcontroller. Describe the register bank of 8051 microcontroller. Describe the addressing modes of ARM7 microcontroller with example.	20	
2.	(a) (b) (c)	Draw and describe the architecture of 8051 microcontroller. Describe the techniques used for memory optimization. Draw the format for status register of MSP430 also write the function of each bit.	8 6 6	
3.	(b)	Draw the interfacing of 8051 microcontroller with seven segment LED. Also write an assembly language program to display 0 to 8 digits. Draw the RTOs kernel with common components also describe them.	8	
	(c)	Describe the functions in Embedded C-programming.	6	
4.	(a) (b) (c) (d)	With diagram describe the hardware/software co-design. Describe the instruction set of ARM7 with example. Describe the function of assemblers, loaders and linkers. Describe the big endian and little endian format of ARM7 microcontroller.	5 5 5 5	
5.	(a) (b)	Draw the interfacing of ARM7 with stepper motor. Also write a program to rotate it in anti-clock wise direction. With neat diagram describe the counting and mutex semaphore.	8	
-	(c)	Describe the different addressing modes of MSP430 with example.	6	
6.	(a) (b)	Draw the format foe CPSR register. Also describe the function of each bit. Draw and describe the architecture of MSP430.	8	

(c) Describe the priority preemptive scheduling algorithm with example.