QP Code: 14927

CERTICAL ETATE
(3 Hours)
TE/ELICTRICAL/SUDA ETAT.S CN [12-11A [Total Marks: 80 Instructions: 1. Question No: 1 is compulsory. 2. Answer any three from the remaining questions. State and prove Sampling Theorem. Draw and explain the block diagram of a digital communication system. 1. a) e) Explain how FSK can be generated using ASK. Explain the need for modulation in a communication system. Generate Huffman's code for the five symbols of a source having (10) probabilities: 0.5, 0.25, 0.125, 0.0625, and 0.0625. Find entropy of the 2. a) source, average code word length and efficiency of the code. b) Explain with neat block diagram the working of a super-heterodyne AM (10) receiver. 3. a) Draw the spectrum of an AM waveform if the modulating signal is  $m(t) = (\cos 2000\pi t + 0.5 \cos 4000\pi t)$  and carrier is  $c(t) = 7.5 \cos (1000\pi t)$ . (10) Calculate total power and power of sidebands. b) What is a balanced modulator? With the help of waveforms, prove that the (10)carrier is getting suppressed. A message 101101 is to be transmitted in cyclic code with a generator (10) polynomial G (D) =  $D^4 + D^3 + 1$ . Obtain the transmitted code word. How many check bits does the encoded message contain? Draw the encoding arrangement for the same. b) Explain Delta modulation transmitter and receiver with the help of a neat (10) block diagram. What is slope overload and hunting error?

b) State and prove the properties of Fourier Transform.

6. Write short notes on any three:

(20)

- a) OFC.
- b) Quantization process.
- c) Shannon Hartley Theorem.
- d) Pre-Emphasis and De-Emphasis.