Vip

Paper / Subject Code: 39705 / APPLICATION OF INTEGRATED CIRCUITS

(3 Hours)

SE | MTRX | SEMIT | CBSGS | SH-2018

14/12/2018

Total Marks: 80

N.B.: (1) Question No.1 is compulsory. (2) Solve any three questions from remaining five questions. (3) Draw neat diagrams and assume suitable data wherever necessary. Justify your assumptions. 1. Attempt any four questions. Explain op-amp as inverting amplifier. (a) 20 Explain the concept of virtual short and virtual ground. (b) Explain features of IC 723. (c) Explain the Principle of oscillator. (d) Enlist merits and demerits of active filters. (e) Explain op-amp amplifier in closed loop configuration. (f) 2. (a) Explain the operation of IC 555 as a stable multivibrator. 10 (b) Explain the working of binary weighted resistor DAC 10 3. (a) Explain sample and hold circuit with wave forms. 8 Explain the operation of voltage to current converter (b) 6 Explain in detail the working of LM337 Voltage Regulator Explain the Operation of dual slope ADC in detail. 10 4. (a) Explain op-amp as sample and hold circuit. 5 (b) Explain the working of VCO IC 566 5 (c) Explain the working of operational amplifier as differentiator. 5. (a) 10 Explain in details the operation of inverting Schmitt trigger. 10 (b) Write short note on any four from following: 6. 20 Effect of negative feedback on frequency response. (a) (b) Classification of filters (c) Instrumentation amplifier (d) LT1070 Monolithic switching regulator.

(e)

PLL IC 565