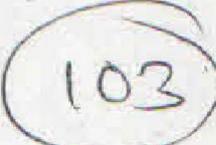
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(3 Hours)

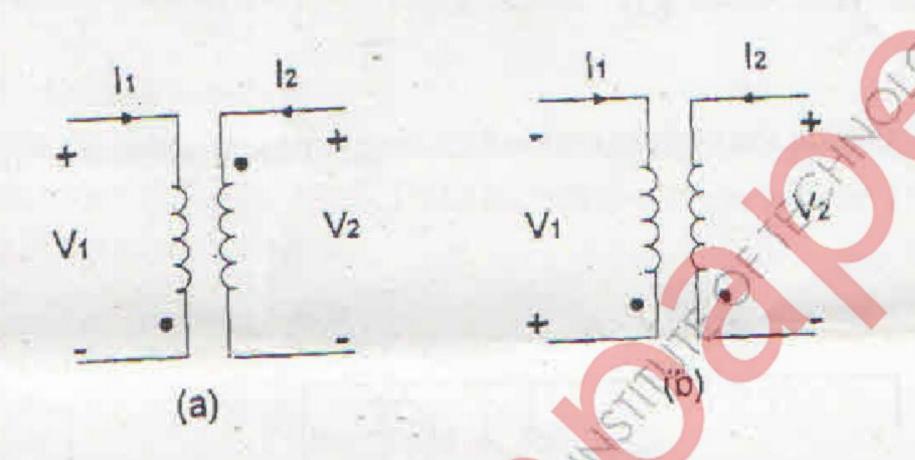
[Total Marks: 80

N.B.: (1) Question no 1 is compulsory

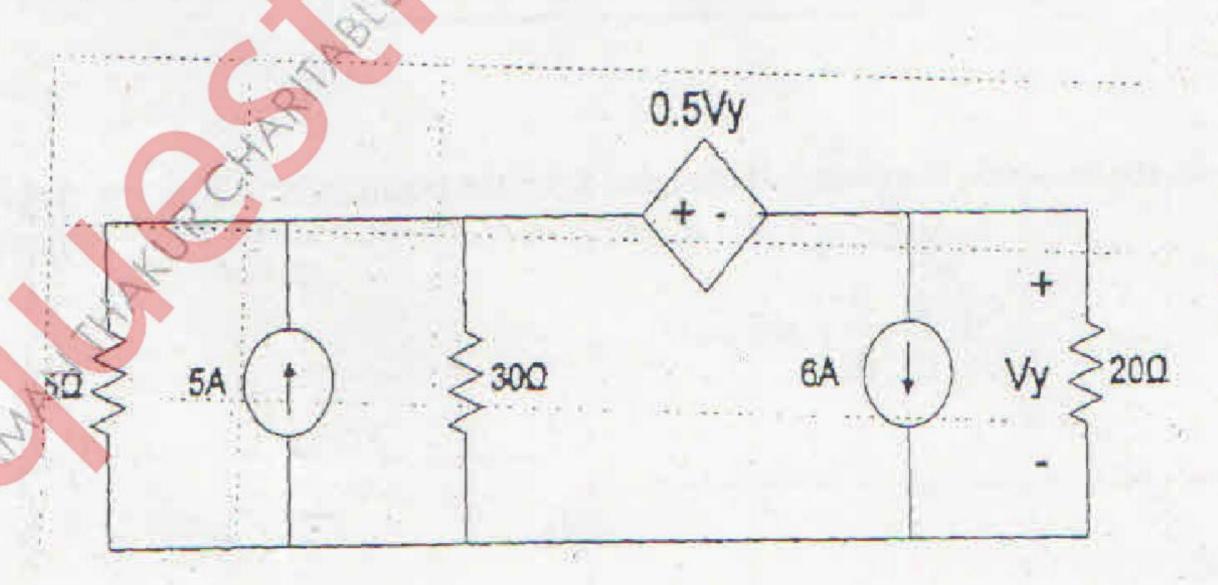
- (2) Solve any three questions from remaining five questions.
- (3) Right figure indicate marks.
- (4) Figures to the right indicate full marks.

1. Attempt the fullowing

- (a) Derive the frequency domain equivalent for inductor with initial current loand capacitor with initial voltage Vo
- (b) Write the equations for V, and V, for circuits shown in figure (a) and (b)



- (c) What are restrictions on pole and zero location for driving point function.
- (d) Write the properties of positive real function.
- 2. (a) Use nodal analysis to find Vy in the given circuit.

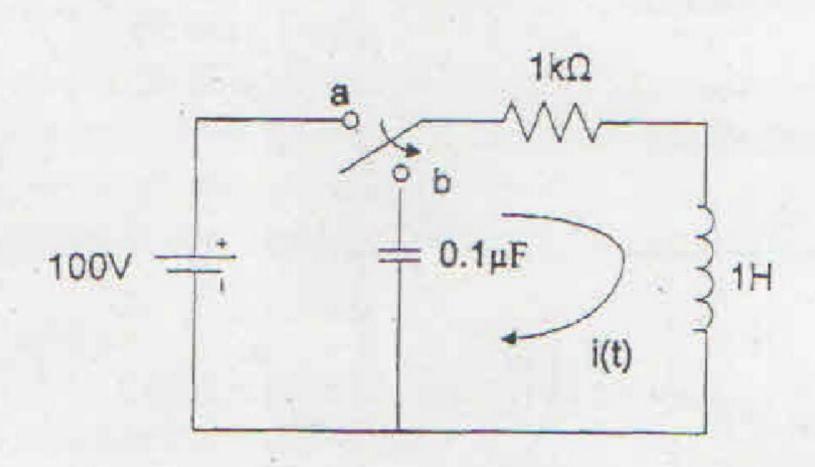


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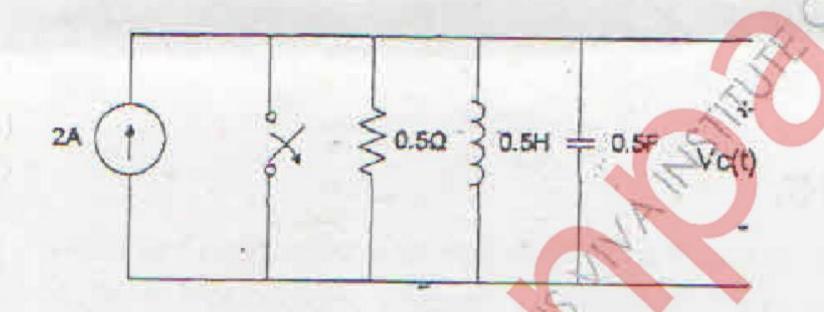
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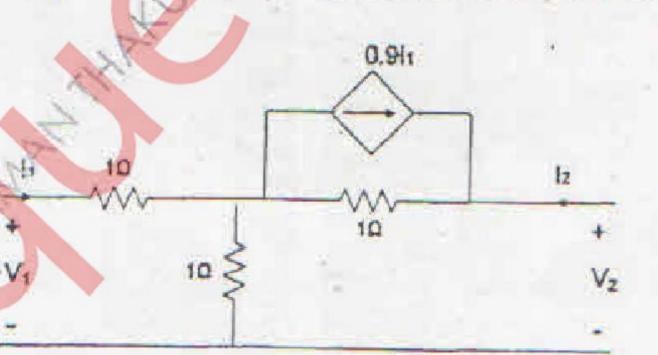
- (c) Derive the response on unit step signal in case of series RL circuit.
- 4. (a) Explain duality in a network
  - (b) Find the condition of reciprocity in case of short circuit parameters. The switch is opened at time t=0. Determine the voltage v(t) for t>0 using Laplace transform in the given network. 10



5. (a) Synthesize the following function by Cauer I and Cauer II form

$$z(s) = \frac{(s+2)(s+5)}{(s+1)(s+3)}$$

(b) Find the Z parameters fur the network shown. Hence find h parameters.



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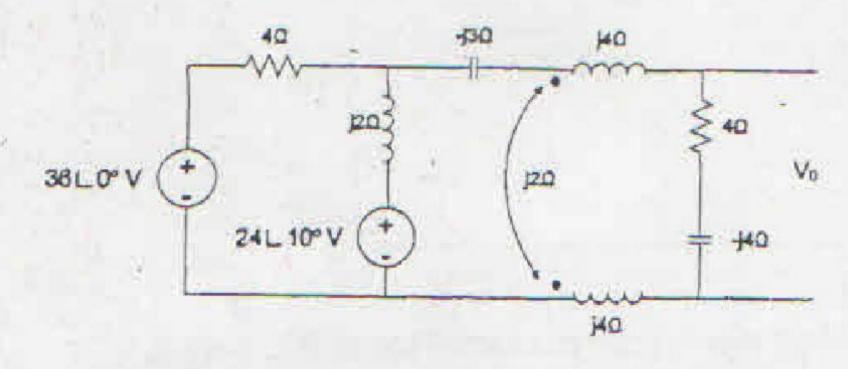
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6. (a) The current I (s) in a network is given by  $I(s) = \frac{2s}{(s+1)(s+2)}$  Plot the pole zero

pattern in s plane and obtain i(t) Find Vo in the network shown



(c) Write the equilibrium equation on KCL and KVL basis for network

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