

[Time: Three Hours]

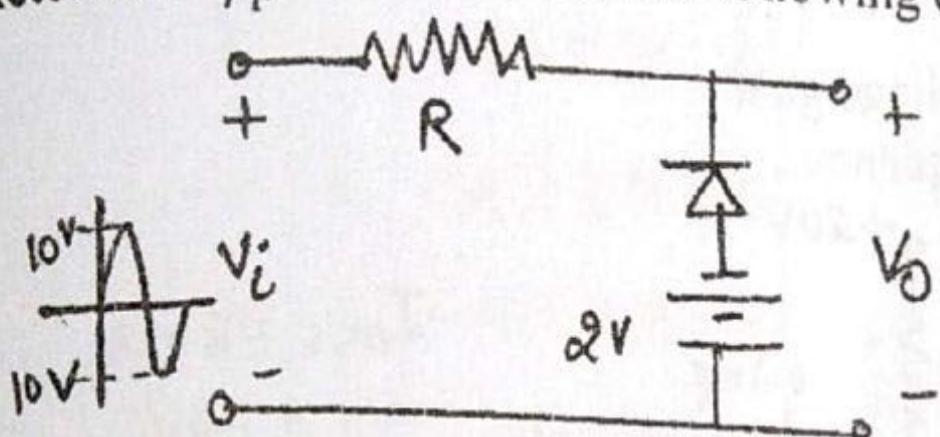
19

[ Marks: 80]

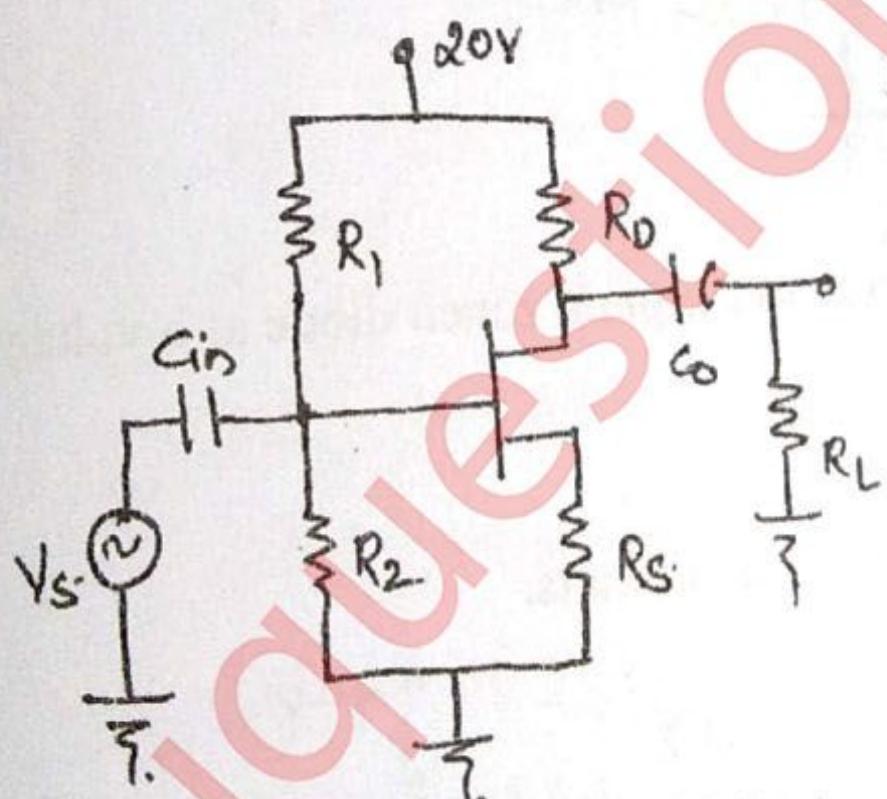
Please check whether you have got the right question paper.

- N.B: 1. Question no one is compulsory.  
 2. Attempt any three questions from the remaining five.  
 3. Assume suitable data if necessary.

- Q.1 a Discuss graphical method of calculating  $h$  parameters. 05  
 b Draw the  $i/p$  and  $o/p$  characteristics of D-MOSFET. 05  
 c Compare CE and CS amplifier. 05  
 d Sketch the  $o/p$  waveform for the following ckt. 05

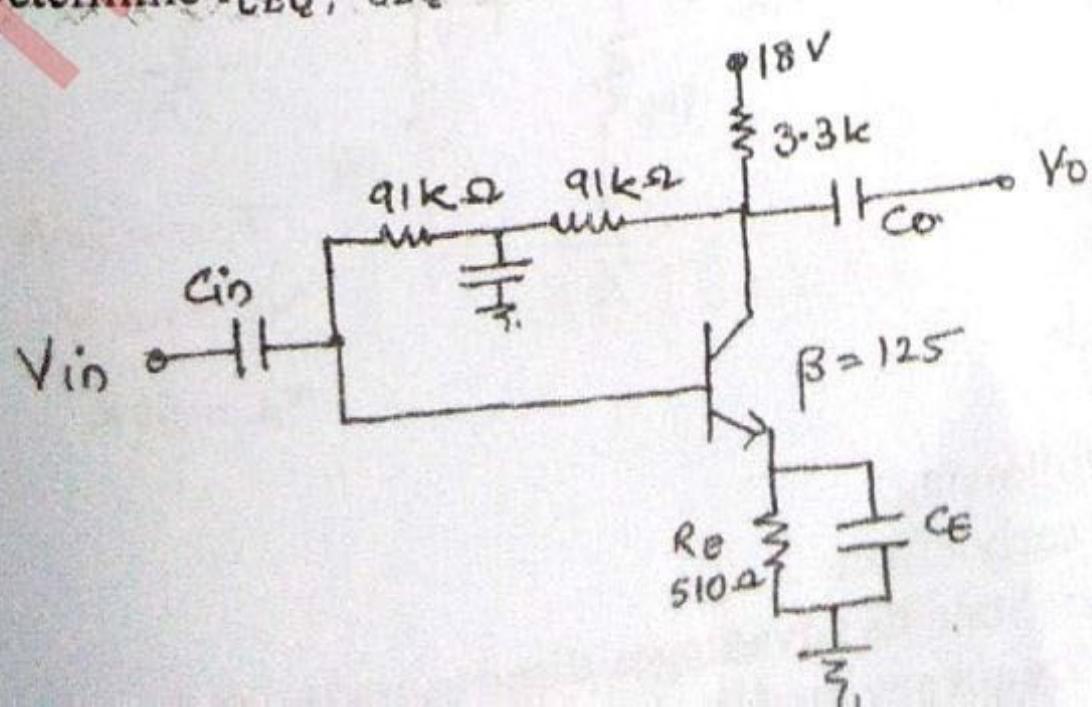


- Q.2 a Calculate  $A_v$ ,  $R_{in}$ ,  $R_{out}$  and  $f_L$  for the following ckt. 10



Given:  $R_1 = 910 \text{ k}\Omega$   
 $R_D = 2.2 \text{ k}\Omega$   
 $R_2 = 220 \text{ k}\Omega$   
 $R_S = 1.2 \text{ k}\Omega$   
 $G_n = 47 \mu\text{f}$   
 $C_o = 10 \mu\text{f}$   
 $C_S = 1 \mu\text{f}$   
 $R_L = 10 \text{ k}\Omega$

- b Determine  $I_{CEQ}$ ,  $V_{CEQ}$  & stability for the given network 10



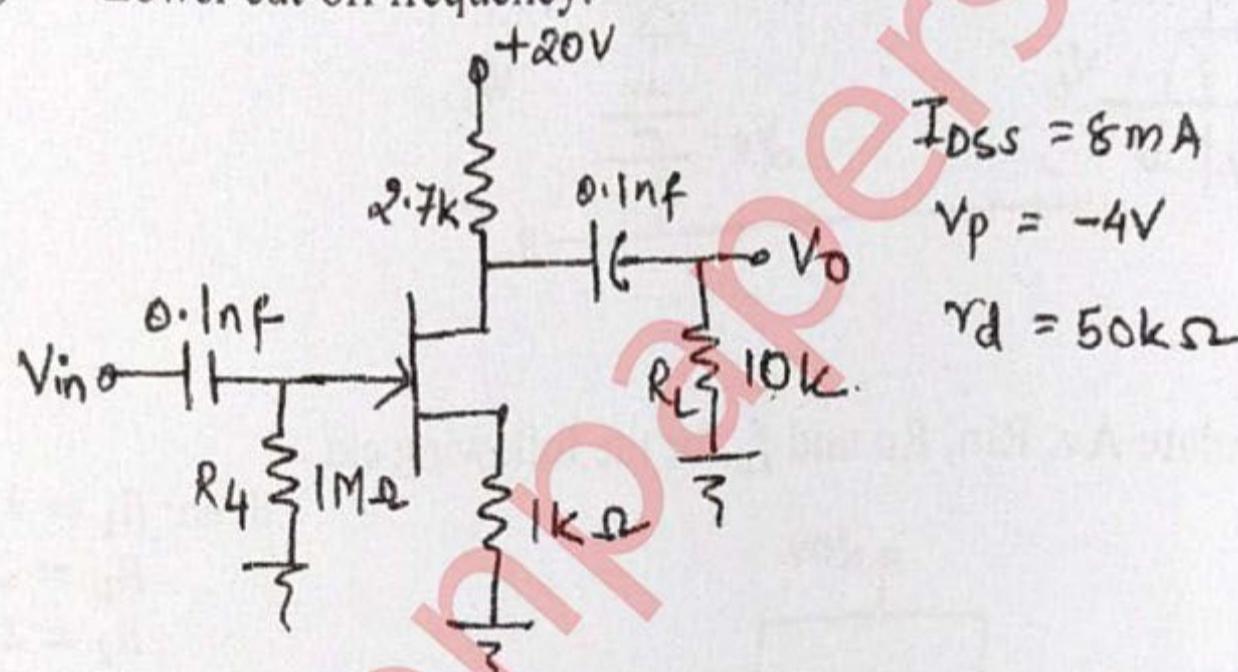
**Q.3** Design a single stage RC coupled CE amplifier to meet the following specifications. 20

- $|Av| \geq 220$
- $S \leq 10, V_o = 4V, f_L = \leq 20Hz$
- Calculate  $Av, R_o$  and  $R_i$  for the above design

**Q.4** a Draw and explain cascode amplifier. Drive expression for gain, input impedance & output impedance. State an application for the same. 10

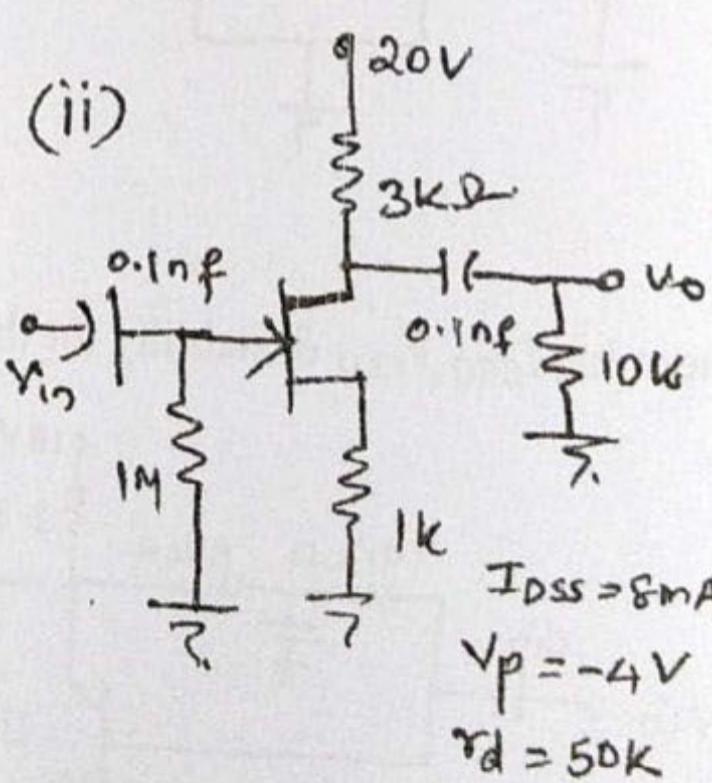
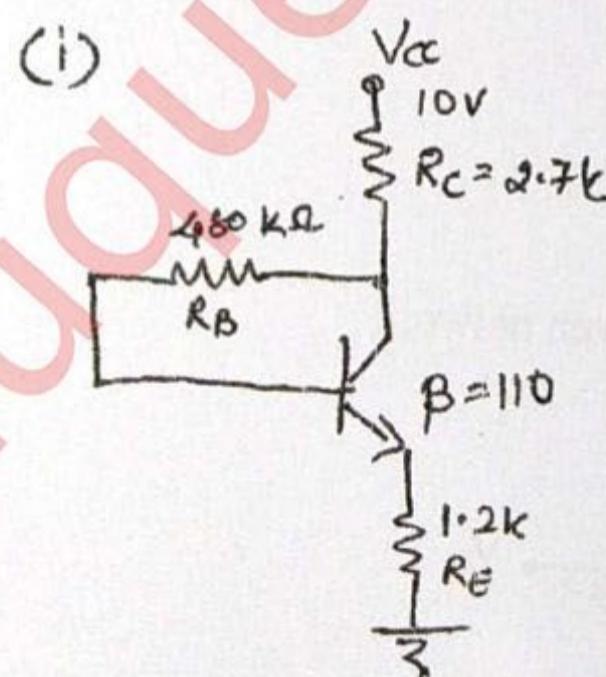
b For the JFET amplifier shown find the following 10

- Q-point
- Mid frequency voltage gain
- Lower cut-off frequency.



**Q.5** a Sketch zener diode characteristics and explain zener diode as a voltage regulator. 08

b Determine the Q point for the following circuits. 12



**Q.6** Write short notes on the following 20

- Explain thermal drift and early effect.
- Draw Darlington amplifier. State its advantages disadvantages and application.
- Draw frequency and high frequency model of JFET.
- Comparison of CB, CC, and CE amplifier.

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**DBEC DATA SHEET**

Transistor type	P <sub>dmax</sub> @ 25°C Watts	I <sub>cmax</sub> @ 25°C volts d.c.	V <sub>ce0</sub> volts d.c.	V <sub>ce0</sub> (S <sub>ce0</sub> ) volts d.c.	V <sub>ce0</sub> volts d.c.	T <sub>j</sub> max °C	D.C. current min. typ. max.	gain min. typ. max.	Signal min. typ. max.	h <sub>f</sub> max.	Y <sub>ds</sub> max.	θ <sub>ciw</sub> °C/W	Derate above 25°C W/W				
2N 3055	115.5	15.0	1.1	100	60	70	90	1	200	20	50	70	15	50	120	1.8	0.7
ECN 055	50.0	5.0	1.0	60	50	55	60	5	200	25	50	100	25	75	125	1.5	3.5
ECN 149	30.0	4.0	1.0	50	40	—	—	8	150	30	50	110	33	60	115	1.2	4.0
ECN 100	5.0	0.7	0.6	70	60	65	—	6	200	50	90	280	50	90	280	0.9	35
BC147A	0.25	0.1	0.25	50	45	50	—	6	125	115	180	220	125	220	260	0.9	—
2N 525(PNP)	0.225	0.5	0.25	85	30	—	—	—	100	35	—	65	—	45	—	—	—
BC147B	0.25	0.1	0.25	50	45	50	—	6	125	200	290	450	240	330	500	0.9	—
<b>Transistor Type</b>																	
BC 147A	2.7 KΩ	18μV	1.5 × 10 <sup>-1</sup>	0.4°C/mW	—	—	—	—	—	—	—	—	—	—	—	2.5	3.5
2N 525 (PNP)	1.4 KΩ	25μV	3.2 × 10 <sup>-1</sup>	0.4°C/mW	—	—	—	—	—	—	—	—	—	—	—	2.0	4.0
BC 147B	4.5 KΩ	30μV	2 × 10 <sup>-1</sup>	0.4°C/mW	—	—	—	—	—	—	—	—	—	—	—	1.1	0.5
ECN 100	50Ω	—	—	—	—	—	—	—	—	—	—	—	—	—	—	0.0	0.0
ECN 149	15Ω	—	—	—	—	—	—	—	—	—	—	—	—	—	—	0.0	0.0
ECN 055	12Ω	—	—	—	—	—	—	—	—	—	—	—	—	—	—	0.0	0.0
2N 3055	6Ω	—	—	—	—	—	—	—	—	—	—	—	—	—	—	0.0	0.0
<b>N-Channel JFET</b>																	
Type	V <sub>ds</sub> max. Volts	V <sub>ds</sub> max. Volts	V <sub>ds</sub> max. Volts	P <sub>d</sub> max. @25°C	T <sub>j</sub> max. °C	I <sub>ds</sub> max. mA	I <sub>ds</sub> (typical)	I <sub>ds</sub> (typical)	I <sub>ds</sub> (typical)	θ <sub>ciw</sub> above 25°C	—	—	—	—	—	—	
2N3822	50	50	50	300 mW	175°C	2 mA	3000 μA	6	50 kΩ	2 mW/°C	—	—	—	—	—	—	
BFW 11 (typical)	30	30	30	300 mW	200°C	7 mA	5600 μA	2.5	50 kΩ	0.59° C/mW	—	—	—	—	—	—	