(Time: 3 Hours) [Total Marks 80]

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- 1. Question No. 1 is Compulsory.
- 2. Solve any THREE from Question No. 2 to 6.
- 3. Draw neat well labeled diagram wherever necessary.

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Discuss in detail SISD, SIMD, MISD, MIMD, SIMT, SPMD, MPMD.	(10)
Write a MPI program to find sum of N numbers.	(10)
Derive the expression for speedup and efficiency by Amdahl's law and comment on the same. Assume suitable data if required.	(10)
Discuss different levels of parallel processing.	(10)
Explain about process synchronization mechanism with Semaphore.	(10)
Define MPI. Explain in detail principles of Message Passing Programming.	(10)
Explain in brief Quantum Computers.	(10)
Define CUDA? Explain in CUDA processor architecture.	(10)
Explain speedup, efficiency and scalability with suitable example.	(10)
Explain in detail Architecture of NVIDIA GPU.	(10)
	Write a MPI program to find sum of N numbers.  Derive the expression for speedup and efficiency by Amdahl's law and comment on the same. Assume suitable data if required.  Discuss different levels of parallel processing.  Explain about process synchronization mechanism with Semaphore.  Define MPI. Explain in detail principles of Message Passing Programming.  Explain in brief Quantum Computers.  Define CUDA? Explain in CUDA processor architecture.  Explain speedup, efficiency and scalability with suitable example.

- Q. 6 Write short note on the following:
  - 1. Nanotechnology
  - 2. Grain packing and scheduling in parallel processing

(20)

3. Data Race

4. OpenMP