

Wednesday, December 11, 2019	10:30 am - 01:30 pm	1T00818 - B.E.(ELECTRICAL)(SEM VIII) (CBSGS) / 52803 - Elective II- 1) Flexible AC Transmission System	71309
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**(3 Hours)**

**[Total Marks: 80]**

**1. Question No.1 is Compulsory**

2. Answer any three out of remaining five questions
3. Assume any suitable data wherever necessary and justify the same
4. Illustrate answer with sketches wherever required

Q.1	Answer all questions	
a)	What is reactive power biasing? Explain with V-Q characteristics.	05
b)	What are the objectives of voltage and phase angle regulators	05
c)	Explain various parameters which limit loading capabilities of transmission line	05
d)	An AC supply with an input AC line voltage of 400V at 50 Hz is connected with three-phase three-wire delta connected balanced load having $Z_L = (4.0+j2.0)$ pu and a base impedance of $5\Omega$ per phase. It is to be realized as a unity power factor load on the AC supply system using shunt connected lossless passive elements (L and/or C)  i) Calculate compensator currents ii) Calculate the values of compensator elements (in farads or Henries)	05
Q.2	a) Explain various types of facts controllers with their objectives in detail b) Show that voltage sensitivity for load reactive power is $\frac{dv}{dq} = \frac{-E/SSC}{1+kr^2(E/SSC)}$	10 10
Q.3	a) Explain power factor correction in single phase systems b) Explain switching converter type VAR generator	10 10
Q.4	a) Explain switching converter type series compensation (SSSC) b) Explain Thyristor Controlled Phase Angle Regulator (TCPAR)	10 10
Q.5	a) Explain power flow and dynamic stability considerations of a transmission interconnection. b) Explain phase balancing and power factor correction of unsymmetrical loads	10 10
Q.6	a) Use phasor diagram to illustrate functioning of UPFC as voltage regulator, line impedance compensator, phase shifter for simultaneous control of voltage, impedance and phase angle b) Explain operation and characteristics of Thyristor Controlled Reactor (TCR). What is the condition to obtain Thyristor Switched Reactor (TSR) from TCR	10 10